Control of ZnO nanowire arrays by Nanosphere Lithography (NSL) on laserproduced ZnO substrates.

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Abstract

Nanosphere lithography (NSL) is a successful technique for fabricating highlyordered arrays of ZnO nanowires typically on sapphire and GaN substrates. In this work, we investigate the use of thin ZnO films deposited on Si by pulsed laser deposition (PLD) as the substrate. This has a number of advantages over the alternatives above, including cost and potential scalability of production and it removes any issue of inadvertent n-type doping of nanowires by diffusion from the substrate. We demonstrate ordered arrays of ZnO nanowires, on ZnO-coated substrates by PLD, using a conventional NSL technique with gold as the catalyst. The nanowires were produced by Vapor Phase Transport (VPT) growth in a tube furnace system and grew only on the areas pre-patterned by Au. We have also investigated the growth of ZnO nanowires using ZnO catalyst points deposited by PLD through an NSL mask on a bare silicon substrate.

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1. Introduction

There is currently a great deal of interest in 1D nanoscale materials with the aim of developing nanodevices with improved functionality. One such potential application is in the development of new field emission (FE) sources for use as miniature X-ray sources or flat panel displays [1,2]. Zinc oxide (ZnO) is a promising candidate for such devices which has applications in various technologies [3] and in particular is well suited for FE applications. However, strong control of the morphology of nanowire arrays produced for this purpose is required. Array density has been shown to have a large impact on the efficiency and uniformity of FE devices [4]. This control over array morphology can be achieved using nanosphere lithography (NSL) and there are many examples in the literature of well ordered, periodical arrays of ZnO nanowires grown via the use of NSL [5-9] to create an initial ordered template of metal islands on a substrate. NSL relies on producing a lithographic mask comprised of a close packed monolayer of polystyrene beads which can be produced with differing aperture sizes and separation. It is often preferable to other patterning techniques such as e-beam lithography and mask lithography [9] due to its low cost and the ability to easily pattern large areas. This technique is most commonly used in conjunction with vapor phase transport growth for ZnO nanowires where, under suitable conditions, the nanowire growth occurs at the metal catalyst point only. NSL has been used to create well ordered arrays of ZnO nanowires on substrates such as sapphire [5-7] and GaN thin films [8], however to our knowledge this technique has not been applied to growth on ZnO substrates, ZnO buffer layers or even directly on Si substrates. The development of ordered arrays on these types of substrate would have a number of advantages such as removing the possibility of unwanted doping from the substrate (e.g., Al from sapphire substrates) and also allowing reductions in cost due to the use of a range of cheaper substrate materials and enable potential scalability to large area substrates [10]. While ordered growth on ZnO substrates is desirable, it presents problems which are not addressed in the literature since these same problems are not encountered for growth on substrates such as sapphire and GaN. This issue is that ZnO naturally presents a highly favourable surface for ZnO nucleation, causing growth to occur on all areas of the substrate making selective growth difficult or impossible. To overcome this issue, very close control over the Zn vapor pressure in the VPT growth is required.

In this paper we present the results of studies attempting to grow well ordered arrays of vertically aligned ZnO nanowires by VPT on ZnO buffer layers deposited by PLD onto Si(100) substrates. Ordered arrays were achieved by patterning the ZnO substrate with Au islands using NSL. These arrays cover large area substrates (of dimensions > 1 cm x 1 cm) and are uniform in terms of nanowire height and density of growth across these areas. The problem of growth over the entire substrate has been overcome by careful placement of the sample in the VPT tube furnace relative to the source materials. We also investigate the possibility of growth of patterned arrays of nanowires without the use of Au catalyst points onto Si substrates. This is a very desirable goal as it entirely removes the possibility of unwanted doping/contamination from the substrate or from metallic catalyst points.

2. Experimental details

Si (100) substrates were cleaned by ultrasonication in acetone and isopropanol for 15 minutes each and then a ZnO buffer layer was deposited by PLD. Due to the

deposition of a ZnO buffer layer, the native oxide layer is not expected to impact result and no attempt was made to remove the native oxide prior to growth. The thickness of the ZnO buffer layer was maintained between 150 nm and 250 nm. PLD was carried out with a frequency quadrupled Nd:YAG laser with a wavelength of 266 nm focussed on to a sintered compressed ZnO target. The fluence was kept at ~3 J/cm^2 . The base pressure of the system was ~5 x 10⁻¹² Pa (5 x 10⁻⁸ mbar) and depositions were carried out at 10 Pa (0.1 mbar) with a 30 sccm flow of oxygen. The samples were heated to 900⁰C before deposition to remove any impurities and deposition was carried out at 300⁰C. The temperature was then ramped up to 700⁰C to anneal the deposited layer and slowly cooled to room temperature.

These substrates were then coated with a close packed monolayer of polystyrene (PS) nanospheres. The spheres used (Thermo Scientific 5100A) were 1 µm in diameter. The nanosphere solution was mixed with ethanol in a 1:1 ratio and carefully applied to the surface of water in a Petri dish. Once in contact with the water surface the spheres are seen to disperse across the surface before organising into areas of close packed monolayer. The ZnO substrate is then immersed in the water and slowly withdrawn through an area where the spheres have packed together. The quality of this layer can be easily verified by visual inspection as these layers display very clear opalescence under white light illumination. The coated substrates are then allowed to dry in air before a layer of Au (of nominal thickness 2.5 nm) is deposited over the nanosphere layer using a bell jar thermal evaporator. To control the size of the deposited Au islands the substrates were held at angles from 10 to 20 degrees relative to the Au source. This allows a rough degree of control over the size of the islands. The nanospheres are finally removed by ultrasonic bath in toluene, leaving only an ordered array of Au islands across the substrate.

VPT growth was carried out on the Au patterned substrates using the method described in [11] at a temperature at 925^oC for one hour and allowed to cool to room temperature over a period of several hours. 0.06g of ZnO and 0.06g graphite powder was mixed thoroughly using a pestle and mortar and loaded into an alumina boat with the substrate loaded facing down approximately 1 cm away from the source powders in the direction of the Ar flow (i.e. downstream).

For the growths carried out without the use of Au catalysts, cleaned Si (100) substrates were coated with a layer of close packed nanospheres as described above and then coated with a layer of ZnO by PLD. For these substrates, the deposition was carried out at room temperature as the polystyrene beads cannot withstand high temperatures. The spheres were removed by ultrasonic bath in toluene and VPT nanowire growth was carried out under the same conditions as above, but with the substrate loaded directly over the source powders as under these conditions growth on non-patterned bare Si/SiO_2 regions is not expected as Si/SiO_2 does not present an attractive surface for the nucleation of ZnO.

Characterisation was carried out using scanning electron microscopy (Karl-Zeiss EVO series).

3. Results and Discussion

3.1 Growth of ordered nanowire arrays on continuous PLD ZnO buffer layers using NSL patterned Au catalysts.

Au was deposited through a NSL mask, leaving islands of Au patterned regions of 3 mm diameter which were then grown upon. Strong growth was seen on the samples

once removed from the VPT furnace. Invariably there were three distinct regions after growth. The first region, that closest to the source powders, is white/grey in colour and is dominated by very strong nanowire growth, however this growth is unaligned and does not show any patterning. It is thought this is due to a high Zn vapour pressure in this region.

The second region is that where ZnO deposits solely on areas catalyzed with Au. Areas not patterned with Au are unchanged post-growth. Where catalyzed, the substrate appeared matt-white, an indicator of nanowire growth. The substrate also retained the opalescent qualities of the NSL mask used to pattern it. This is a strong indicator of spatially ordered growth, as deduced from comparison of SEM data with visual inspection. It is in this region where ordered, aligned nanowire growth occurs.

The final region is the furthest from the source powders. Some ZnO nucleation occurs here but does not develop into nanowires. Visually its appearance is unchanged. It is thought that this is due to low Zn vapour pressure, as it has been depleted through deposition on other regions of the substrate.

Growths were carried out using arrays of Au catalyst points deposited at various angles with respect to the Au source as described in [12]. This produced samples with Au islands of varying sizes depending on both the angle of deposition and the orientation of nanosphere domains on each sample. It was observed that on samples where Au was deposited perpendicular to the source that multiple nanowires would grow from each point in the catalyst pattern, in fact the pattern itself is obscured except for regions with quite short nanowires. Depositing Au at higher angles reduced multiwire nucleations but only to a certain degree. Au deposited at an angle of $\sim 10^{\circ}$ would result in nanowire arrays that would also have multiple

nucleations from each point, however there were fewer nanowires growing from each point and the hexagonal pattern was still apparent. There are also regions where a single nanowire would grow from a single dot. Increasing the deposition angle to $\sim 16^{\circ}$ or higher further reduced the occurrence of multiple nucleation from each point, however it introduced another issue impacting the quality of the arrays grown. In this case areas of the sample were observed with no nanowire growth at all. It is suspected that this is due to the random orientation of nanosphere domains across the sample, causing some of the nucleation points to be too small to catalyze growth or possibly not be present at all.

Growths were also carried out with varying thicknesses of Au. Two nominal thicknesses were used, 2.5 nm and 5 nm. The growths using the thicker, 5 nm, coating were seen to be less spatially ordered than those coated with 2.5 nm thickness of Au. We believe that this is due to the thicker Au layers beading up at high temperature and moving slightly across the surface of the substrate, causing growth of multiple rods from each point and some growth from areas not initially patterned by Au.

The best results were achieved using 2.5 nm thick Au catalyst pattern deposited ~10° from normal as demonstrated in Fig 1 and Fig 2. These figures show areas of highly patterned nanowires in plan view with tilted views inset. It is clear from these images that there is multiple wire growth from many catalyst points and that there is no growth where the substrate is not catalysed with Au. Fig. 1 also includes XRD data showing θ -2 θ and rocking curve of ZnO (0002) peak. In plan view the hexagonal pattern is clear and the majority of nanowires appear as points due to good alignment. There are also a number of defects in the pattern visible, including nanowires that are not perfectly aligned.

The patterned arrays of nanowires however do not extend over the full substrate. The regions of successful growth are confined to regions that are approximately 4 or 5 mm wide and are found approximately 5 mm away from the source powders.

3.2 Growth of nanowire arrays on NSL patterned ZnO buffer layers deposited by PLD with no Au catalyst.

The samples were examined by SEM both before and after VPT growth. Once the nanosphere layer was removed from the sample it had the same appearance as a bare Si substrate. However when examined by SEM a ZnO honeycomb pattern was noted across the sample. Unlike Au deposition through a nanosphere mask, which results in discrete islands across the sample, depositing ZnO through this mask by PLD resulted in a honeycomb pattern. After VPT growth the sample had taken on a matt white appearance. SEM studies showed that randomly oriented ZnO nanowire growth occurred all over the sample and not solely at NSL patterned regions. Low temperature growth is needed as to not destroy the PS nanospheres, however it results in poor texture of ZnO seeds [13], which we expect caused unaligned growth. In areas near the edge of the substrate where nanowire growth did not occur it was clear that the ZnO honeycomb pattern formed a ZnO film when subjected to VPT growth. This film growth completely obscured the NSL pattern making patterned ordered arrays impossible to grow with this method.

4. Conclusions

In summary, we have presented a procedure for producing well ordered arrays of vertically aligned ZnO nanowires on an NSL patterned Au catalyzed ZnO buffer layer deposited by PLD on Si. Patterned arrays of ZnO nanowires have been obtained but much more control over parameters such as the size of the Au catalyst points and Zn vapor pressure inside the VPT furnace are required to enable reproducibility and scalability. The second route investigated involved depositing NSL patterned ZnO seeds onto Si directly by PLD. This method was unsuccessful because (a) the low deposition temperatures required to deposit through the PS nanosphere mask mean that the deposited ZnO seeds were unaligned, leading to randomly oriented VPT growth, and also (b) because ZnO film growth from the NSL honeycomb patterned ZnO initial deposit leads to complete substrate coverage and subsequent unpatterned nanowire growth over the entire substrate.

Further work towards the production of large area patterned arrays of nanowires will include replacing PS with silica as the nanosphere material, allowing high temperature PLD depositions though an NSL mask, allowing for the deposition of ordered arrays of high quality textured/aligned ZnO seeds to catalyze subsequent VPT growth.

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List of Figure Captions

Fig 1: ZnO nanowire array on continuous PLD ZnO buffer layer using NSL patterned Au catalysts (plan view), insets show 45^{0} tilted view (upper left, scale bar indicates 2 μ m) and XRD data (lower right, showing both θ -2 θ and rocking curve of ZnO (0002) peak) illustrating ordering and alignment of nanowire growth.

Fig 2: ZnO nanowire array on continuous PLD ZnO buffer layer using NSL patterned Au catalysts (plan view), insets show 45^{0} tilted views at different magnifications (scale bar in inset at upper left is 3 μ m and at lower right is 2 μ m) illustrating ordered growth.

Fig 3: ZnO film covering substrate after VPT growth on a NSL patterned ZnO buffer layer in area with no observed VPT nanowire growth. Inset shows the honeycomb ZnO patterned buffer layer prior to growth.



Figure 1.



Figure 2.



Figure 3